

		Subclass	ISSUE CLASSIFICATION
		Class	

**PATENT NUMBER**

**U.S. UTILITY** Patent Application

PN 8 O.P.E.  
SCANNED H5 Q.A. Cc

**PATENT DATE**

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/4531860		702	162	2857	Shah West

## Jorge Figueredo

## Method for testing multi-chip packages

PTO-2040  
12/99

## **ISSUING CLASSIFICATION**

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER		DRAWINGS			CLAIMS ALLOWED	
		Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.					<b>NOTICE OF ALLOWANCE MAILED</b>	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent No. _____					<b>ISSUE FEE</b> Amount Due _____ Date Paid _____	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.					<b>ISSUE BATCH NUMBER</b> General Instruments Corporation _____ (Date) _____	

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